

ABSTRACT

A driving circuit for switches of direct current fan motor is disclosed. The driving circuit includes a plurality of switches, a first control circuit, and a second circuit. The switches are driven by a first pulse width modulation signal and a second pulse width modulation signal, and they are electrically connected with the direct current fan motor in a bridge manner. A third pulse width modulation signal is used to drive the first control circuit connected to at least one of the switches driven by the first pulse width modulation signal. A fourth pulse width modulation signal is used to drive the second control circuit connected to at least one of the switches driven by the second pulse width modulation signal. Especially, either the first pulse width modulation signal or the second pulse width modulation signal is selected as the third pulse width modulation signal or the fourth pulse width modulation signal.